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09/974806

10/12/01

ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.

PATENT DATE

EM
SCANNED

NSI Q.A.

SW

JCS88

APPLICATION NO. 09/974806	CONT/PRIOR D F	CLASS 345	SUBCLASS Z11	ART UNIT 2674	EXAMINER DINH, Duc
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APPLICANTS

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Flat display panel having internal lower supply circuit for reducing power consumption

PTO-2040
12/89

ISSUING CLASSIFICATION												
ORIGINAL				CROSS REFERENCE(S)								
CLASS		SUBCLASS		CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)							
INTERNATIONAL CLASSIFICATION												

☐ Continued on Issue Slip Inside File Jacket

<input checked="" type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		NOTICE OF ALLOWANCE MAILED 	
<input checked="" type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. <u>6522314</u>	_____ (Primary Examiner) (Date)		ISSUE FEE Amount Due Date Paid	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner) (Date)		ISSUE BATCH NUMBER	
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